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VILLEGAS KANE

FPGA-based Implementation of Signal Processing Systems John Wiley & Sons
PES College of Engineering is organizing an International Conference on Emerging Research in Electronics, Computer Science and Technology (ICERECT-12) in Mandya and merging the event with Golden Jubilee of the Institute. The Proceedings of the Conference presents high quality, peer reviewed articles from the field of Electronics, Computer Science and Technology. The book is a compilation of research papers from the cutting-edge technologies and it is targeted towards the

scientific community actively involved in research activities.

Digital Signal Processing with Field Programmable Gate Arrays Springer

This book constitutes the refereed proceedings of the 12th International Symposium on Applied Reconfigurable Computing, ARC 2016, held in Rio de Janeiro, Brazil, in March 2016. The 20 full papers presented in this volume were carefully reviewed and selected from 47 submissions. They are organized in topical headings named: video and image processing; fault-tolerant systems; tools and architectures; signal processing; and multicore systems. In addition, the book contains 3 invited papers and 8 poster papers on funded RD running and completed projects.

Cryptographic Hardware and Embedded Systems -- CHES 2010

Springer

This volume presents the proceedings of the CLAIB 2016, held in Bucaramanga, Santander, Colombia, 26, 27 & 28 October 2016. The proceedings, presented by the Regional Council of Biomedical Engineering for Latin America (CORAL), offer research findings, experiences and activities between institutions and universities to develop Bioengineering, Biomedical Engineering and related sciences. The conferences of the American Congress of Biomedical Engineering are sponsored by the International Federation for Medical and Biological Engineering (IFMBE), Society for Engineering in Biology and Medicine (EMBS) and the Pan

American Health Organization (PAHO), among other organizations and international agencies to bring together scientists, academics and biomedical engineers in Latin America and other continents in an environment conducive to exchange and professional growth.

Emerging Research in Electronics, Computer Science and Technology
Elsevier

This book provides an insight into the 'hot' field of Radio Frequency Identification (RFID) Systems In this book, the authors provide an insight into the field of RFID systems with an emphasis on networking aspects and research challenges related to passive Ultra High Frequency (UHF) RFID systems. The book reviews various algorithms, protocols and design solutions that have been developed within the area, including most recent advances. In addition, authors cover a wide range of recognized problems in RFID industry, striking a balance between theoretical and practical coverage. Limitations of the technology and state-of-the-art solutions are identified and new research opportunities are addressed. Finally, the book is authored by experts and respected

researchers in the field and every chapter is peer reviewed. Key Features: Provides the most comprehensive analysis of networking aspects of RFID systems, including tag identification protocols and reader anti-collision algorithms Covers in detail major research problems of passive UHF systems such as improving reading accuracy, reading range and throughput Analyzes other "hot topics" including localization of passive RFID tags, energy harvesting, simulator and emulator design, security and privacy Discusses design of tag antennas, tag and reader circuits for passive UHF RFID systems Presents EPCGlobal architecture framework, middleware and protocols Includes an accompanying website with PowerPoint slides and solutions to the problems <http://www.site.uottawa.ca/~mbolic/RFIDBook/> This book will be an invaluable guide for researchers and graduate students in electrical engineering and computer science, and researchers and developers in telecommunication industry.

Rapid Prototyping of Digital Systems

Springer Science & Business Media

A guide to applying software design principles and coding practices to VHDL to

improve the readability, maintainability, and quality of VHDL code. This book addresses an often-neglected aspect of the creation of VHDL designs. A VHDL description is also source code, and VHDL designers can use the best practices of software development to write high-quality code and to organize it in a design. This book presents this unique set of skills, teaching VHDL designers of all experience levels how to apply the best design principles and coding practices from the software world to the world of hardware. The concepts introduced here will help readers write code that is easier to understand and more likely to be correct, with improved readability, maintainability, and overall quality. After a brief review of VHDL, the book presents fundamental design principles for writing code, discussing such topics as design, quality, architecture, modularity, abstraction, and hierarchy. Building on these concepts, the book then introduces and provides recommendations for each basic element of VHDL code, including statements, design units, types, data objects, and subprograms. The book covers naming data objects and functions, commenting

the source code, and visually presenting the code on the screen. All recommendations are supported by detailed rationales. Finally, the book explores two uses of VHDL: synthesis and testbenches. It examines the key characteristics of code intended for synthesis (distinguishing it from code meant for simulation) and then demonstrates the design and implementation of testbenches with a series of examples that verify different kinds of models, including combinational, sequential, and FSM code. Examples from the book are also available on a companion website, enabling the reader to experiment with the complete source code.

Trends in Intelligent Robotics CRC Press

Research and innovation in areas such as circuits, microsystems, packaging, biocompatibility, miniaturization, power supplies, remote control, reliability, and lifespan are leading to a rapid increase in the range of devices and corresponding applications in the field of wearable and implantable biomedical microsystems, which are used for monitoring, diagnosing,

and controlling the health conditions of the human body. This book provides comprehensive coverage of the fundamental design principles and validation for implantable microsystems, as well as several major application areas. Each component in an implantable device is described in details, and major case studies demonstrate how these systems can be optimized for specific design objectives. The case studies include applications of implantable neural signal processors, brain-machine interface (BMI) systems intended for both data recording and treatment, neural prosthesis, bladder pressure monitoring for treating urinary incontinence, implantable imaging devices for early detection and diagnosis of diseases as well as electrical conduction block of peripheral nerve for chronic pain management. Implantable Biomedical Microsystems is the first comprehensive coverage of bioimplantable system design providing an invaluable information source for researchers in Biomedical, Electrical, Computer, Systems, and Mechanical Engineering as well as engineers involved in design and development of wearable and implantable bioelectronic devices and,

more generally, teams working on low-power microsystems and their corresponding wireless energy and data links. First time comprehensive coverage of system-level and component-level design and engineering aspects for implantable microsystems. Provides insight into a wide range of proven applications and application specific design trade-offs of bioimplantable systems, including several major case studies Enables Engineers involved in development of implantable electronic systems to optimize applications for specific design objectives.

Creating Autonomous Vehicle Systems MIT Press

These proceedings gather invited and contributed talks presented at the XXI DAE-BRNS High Energy Physics Symposium, which was held at the Indian Institute of Technology Guwahati in December 2014. The contributions cover many of the most active research areas in particle physics, namely (i) Electroweak Physics; (ii) QCD and Heavy Ion Physics; (iii) Heavy Flavour Physics and CP Violation; (iv) Neutrino Physics; (v) Astro-particle Physics and Cosmology; (vi)

Formal Theory; (vii) Future Colliders and New Machines; and (viii) BSM Physics: SUSY, Extra Dimensions, Composites etc. The DAE-BRNS High Energy Physics Symposium, widely considered to be one of the premiere symposiums organised in India in the field of elementary particle physics, is held every other year and supported by the Board of Research in Nuclear Sciences, Department of Atomic Energy, India. Roughly 250 physicists and researchers participated in the 21st Symposium, discussing the latest advancements in the field in 18 plenary review talks, 15 invited mini-review talks and approximately 130 contributed presentations. Bringing together the essential content, the book offers a valuable resource for both beginning and advanced researchers in the field.

Reconfigurable Computing: Architectures, Tools and Applications Springer

This book constitutes the proceedings of the 6th International Symposium on Reconfigurable Computing: Architectures, Tools and Applications, ARC 2010, held in Bangkok Thailand, in March 2010. The 42 papers presented, consisting of 26 full and 16 short papers, were carefully reviewed

and selected from numerous submissions. The topics covered are practical applications of the RC technology, RC architectures, TC design methodologies and tools, and RC education.

A Route to Chaos Using FPGAs

Springer Science & Business Media
Field Programmable Gate Arrays (FPGAs) are currently recognized as the most suitable platform for the implementation of complex digital systems targeting an increasing number of industrial electronics applications. They cover a huge variety of application areas, such as: aerospace, food industry, art, industrial automation, automotive, biomedicine, process control, military, logistics, power electronics, chemistry, sensor networks, robotics, ultrasound, security, and artificial vision. This book first presents the basic architectures of the devices to familiarize the reader with the fundamentals of FPGAs before identifying and discussing new resources that extend the ability of the devices to solve problems in new application domains. Design methodologies are discussed and application examples are included for some of these domains, e.g.,

mechatronics, robotics, and power systems.

Better Software. Faster! CRC Press

A practical and fascinating book on a topic at the forefront of communications technology. Field-Programmable Gate Arrays (FPGAs) are on the verge of revolutionizing digital signal processing. Novel FPGA families are replacing ASICs and PDSs for front-end digital signal processing algorithms at an accelerating rate. The efficient implementation of these algorithms is the main goal of this book. It starts with an overview of today's FPGA technology, devices, and tools for designing state-of-the-art DSP systems. Each of the book's chapter contains exercises. The VERILOG source code and a glossary are given in the appendices.

Hardware/Software Co-Design and Optimization for Cyberphysical Integration in Digital Microfluidic Biochips Springer

Science & Business Media

The LNCS series reports state-of-the-art results in computer science research, development, and education, at a high level and in both printed and electronic form. Enjoying tight cooperation with the R & D community, with numerous

individuals, as well as with prestigious organizations and societies, LNCS has grown into the most comprehensive computer science research forum available. The scope of LNCS, including its subseries LNAI and LNBI, spans the whole range of computer science and information technology including interdisciplinary topics in a variety of application fields. The type of material published traditionally includes proceedings (published in time for the respective conference) post-proceedings (consisting of thoroughly revised final full papers) research monographs (which may be based on outstanding PhD work, research projects, technical reports, etc.) More recently, several color-cover sublines have been added featuring, beyond a collection of papers, various added-value components; these sublines include tutorials (textbook-like monographs or collections of lectures given at advanced courses) state-of-the-art surveys (offering complete and mediated coverage of a topic) hot topics (introducing emergent topics to the broader community) In parallel to the printed book, each new volume is published electronically in LNCS

Online. Book jacket.

Advances in Computation and Intelligence Springer Science & Business Media

Many different kinds of FPGAs exist, with different programming technologies, different architectures and different software. Field-Programmable Gate Array Technology describes the major FPGA architectures available today, covering the three programming technologies that are in use and the major architectures built on those programming technologies. The reader is introduced to concepts relevant to the entire field of FPGAs using popular devices as examples. Field-Programmable Gate Array Technology includes discussions of FPGA integrated circuit manufacturing, circuit design and logic design. It describes the way logic and interconnect are implemented in various kinds of FPGAs. It covers particular problems with design for FPGAs and future possibilities for new architectures and software. This book compares CAD for FPGAs with CAD for traditional gate arrays. It describes algorithms for placement, routing and optimization of FPGAs. Field-Programmable Gate Array Technology

describes all aspects of FPGA design and development. For this reason, it covers a significant amount of material. Each section is clearly explained to readers who are assumed to have general technical expertise in digital design and design tools. Potential developers of FPGAs will benefit primarily from the FPGA architecture and software discussion. Electronics systems designers and ASIC users will find a background to different types of FPGAs and applications of their use.

Effective Coding with VHDL John Wiley & Sons

This book is one of the first technical overviews of autonomous vehicles written for a general computing and engineering audience. The authors share their practical experiences designing autonomous vehicle systems. These systems are complex, consisting of three major subsystems: (1) algorithms for localization, perception, and planning and control; (2) client systems, such as the robotics operating system and hardware platform; and (3) the cloud platform, which includes data storage, simulation, high-definition (HD) mapping, and deep learning model

training. The algorithm subsystem extracts meaningful information from sensor raw data to understand its environment and make decisions as to its future actions. The client subsystem integrates these algorithms to meet real-time and reliability requirements. The cloud platform provides offline computing and storage capabilities for autonomous vehicles. Using the cloud platform, new algorithms can be tested so as to update the HD map—in addition to training better recognition, tracking, and decision models. Since the first edition of this book was released, many universities have adopted it in their autonomous driving classes, and the authors received many helpful comments and feedback from readers. Based on this, the second edition was improved by extending and rewriting multiple chapters and adding two commercial test case studies. In addition, a new section entitled “Teaching and Learning from this Book” was added to help instructors better utilize this book in their classes. The second edition captures the latest advances in autonomous driving and that it also presents usable real-world case studies to help readers better understand how to utilize their lessons in

commercial autonomous driving projects. This book should be useful to students, researchers, and practitioners alike. Whether you are an undergraduate or a graduate student interested in autonomous driving, you will find herein a comprehensive overview of the whole autonomous vehicle technology stack. If you are an autonomous driving practitioner, the many practical techniques introduced in this book will be of interest to you. Researchers will also find extensive references for an effective, deeper exploration of the various technologies.

Hands-on Experience with Altera FPGA Development Boards Springer

This book describes the implementation of green IT in various human and industrial domains. Consisting of four sections: “Development and Optimization of Green IT”, “Modelling and Experiments with Green IT Systems”, “Industry and Transport Green IT Systems”, “Social, Educational and Business Aspects of Green IT”, it presents results in two areas – the green components, networks, cloud and IoT systems and infrastructures; and the industry, business, social and

education domains. It discusses hot topics such as programmable embedded and mobile systems, sustainable software and data centers, Internet servicing and cyber social computing, assurance cases and lightweight cryptography in context of green IT. Intended for university students, lecturers and researchers who are interested in power saving and sustainable computing, the book also appeals to engineers and managers of companies that develop and implement energy efficient IT applications.

Mathematical and Engineering Methods in Computer Science KIT

Scientific Publishing
Reconfigurable Computing Systems Engineering: Virtualization of Computing Architecture describes the organization of reconfigurable computing system (RCS) architecture and discusses the pros and cons of different RCS architecture implementations. Providing a solid understanding of RCS technology and where it’s most effective, this book: Details the architecture organization of RCS platforms for application-specific workloads Covers the process of the architectural synthesis of hardware

components for system-on-chip (SoC) for the RCS Explores the virtualization of RCS architecture from the system and on-chip levels Presents methodologies for RCS architecture run-time integration according to mode of operation and rapid adaptation to changes of multi-parametric constraints Includes illustrative examples, case studies, homework problems, and references to important literature A solutions manual is available with qualifying course adoption. Reconfigurable Computing Systems Engineering: Virtualization of Computing Architecture offers a complete road map to the synthesis of RCS architecture, exposing hardware design engineers, system architects, and students specializing in designing FPGA-based embedded systems to novel concepts in RCS architecture organization and virtualization.

XXI DAE-BRNS High Energy Physics Symposium Now Publishers Inc

The recent rise of "smart" products has been made possible through tight co-design of hardware and software. The growing amount of software and hence processors in applications all around us allows for increased flexibility in the

application functionality through its life cycle. Not so long ago a device felt outdated after you owned it for a couple of months. Today, a continuous stream of new software applications and updates make products feel truly "smart". The result is an almost magical user experience where the same product can do more today than it could do yesterday. In this book we dive deep into a key methodology to enable concurrent hardware/software development by decoupling the dependency of the software development from hardware availability: virtual prototyping. The ability to start software development much earlier in the design cycle drives a true "shift-left" of the entire product development schedule and results in better products that are available earlier in the market.

Throughout the book, case studies illustrate how virtual prototypes are being deployed by major companies around the world. If you are interested in a quick feel for what virtual prototyping has to offer for practical deployment, we recommend picking a few case studies to read, before diving into the details of the methodology.

Of course, this book can only offer a small snapshot of virtual prototype use cases for faster software development. However, as most software bring-up, debug and test principles are similar across markets and applications, it is not hard to realize why virtual prototypes are being leveraged whenever software is an intrinsic part of the product functionality, after reading this book.

Reconfigurable Computing Systems Engineering Springer Nature

This volume contains the papers selected for the 13 FIRA Robot World Congress, held at Amrita Vishwa Vidyapeetham Bangalore, India, September 15-17, 2010. The Federation of International Robot-soccer Association (FIRA - www.fira.net) is a non-profit organization that annually organizes robotic competitions and meetings around the globe. The robot soccer competitions started in 1996, and FIRA was established on, June 5, 1997. The robot soccer competitions are aimed at promoting the spirit of science and technology to the younger generation. The congress is a forum to share ideas and future directions

of technologies, and to enlarge the human networks in the robotics area. The objectives of the FIRA Cup and Congress are to explore the technical developments and achievements in the field of robotics, and provide participants with a robot festival including technical presentations, robot soccer competitions, and exhibits under the theme "Where Theory and Practice Meet." FIRA India aims to propagate and popularize robotics and robotic competitions across India.

Green IT Engineering: Social, Business and Industrial Applications John Wiley & Sons

The purpose of this introductory book is to couple the teaching of chaotic circuit and systems theory with the use of field programmable gate arrays (FPGAs). As such, it differs from other texts on chaos: first, it puts emphasis on combining theoretical methods, simulation tools and physical realization to help the reader gain an intuitive understanding of the properties of chaotic systems. Second, the "medium" used for physical realization is the FPGA. These devices are massively parallel architectures that can be configured to realize a variety of logic

functions. Hence, FPGAs can be configured to emulate systems of differential equations. Nevertheless maximizing the capabilities of an FPGA requires the user to understand the underlying hardware and also FPGA design software. This is achieved by the third distinctive feature of this book: a lab component in each chapter. Here, readers are asked to experiment with computer simulations and FPGA designs, to further their understanding of concepts covered in the book. This text is intended for graduate students in science and engineering interested in exploring implementation of nonlinear dynamical (chaotic) systems on FPGAs.

Proceedings of SLIP '03 Springer

This volume contains the post-conference proceedings of the 10th Doctoral Workshop on Mathematical and Engineering Methods in Computer Science, MEMICS 2015, held in Telč, Czech Republic, in October 2015. The 10 thoroughly revised full papers were carefully selected out of 25 submissions and are presented together with 3 invited papers. The topics covered include:

security and safety, bioinformatics, recommender systems, high-performance and cloud computing, and non-traditional computational models (quantum computing, etc.).

Advanced Computing Institute of Electrical & Electronics Engineers(IEEE)

Volumes CCIS 51 and LNCS 5812

constitute the proceedings of the Fourth International Symposium on Intelligence Computation and Applications, ISICA 2009, held in Huangshi, China, during October 23-25. ISICA 2009 attracted over 300 submissions. Through rigorous reviews, 58 papers were included in LNCS 5821, and 54 papers were collected in CCIS 51. ISICA conferences are one of the first series of international conferences on computational intelligence that combine elements of learning, adaptation, evolution and fuzzy logic to create programs as alternative solutions to artificial intelligence.